

1474
22/7/14

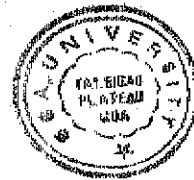
GOA UNIVERSITY

RESULT REGISTER FOR M. E. IN ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN MAY, 2014
COLLEGE : GOA COLLEGE OF ENGINEERING, FARMAGUDI, GOA.

Seat No.	P.R.No.	Name of the Candidate	Semiconductor Devices		VLSI Design & Concept		Hardware Description Languages		VLSI Technology		Design of Analog / Mixed Mode VLSI Circuits		VLSI Design Lab		Hardware Description Languages Lab		SGPA	Remarks							
			MEMIC 1.1		MEMIC 1.2		MEMIC 1.3		MEMIC 1.4		MEMIC 1.5		MEMIC 1.6		MEMIC 1.7										
			Internal	Theory	Internal	Theory	Internal	Theory	Internal	Theory	Internal	Theory	Internal	Practical	Internal	Practical									
1	200707482	MAIK VISHAL RAMESH	2	CC +	6	CC +	2	BC +	8	BC +	2	CC +	8	CC +	2	CC +	2	BB -	→	2	CC +	2	CC +	5.42	PASSES

Grade	Grade Point	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By P. D. D.
Checked By [Signature]
Date 18/7/2014



[Signature]
18/7/2014
S. S. J. Figueiredo
Asstt. Registrar - E(Prof)

[Signature]
Donald A. E. Rodrigues
Controller of Examinations

[Signature]
Prof. V. P. Kamat
Registrar

ABS - Absent; +- Grade Carried Over
SGPA : Semester Grade Point Average
CGPA : Cumulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college.

970
22/07/2014

Maya
NB/WB/copy to HETCD